

MDC100G

Rectifier Diode Module

V _{RRM}	1200 to 2000V
IFAV	100 Amp

IFRMS 155 Amp

Features

- Aluminum oxide DBC
- Glass passivated chip

Applications

- Non-controllable rectifiers for AC/DC
- Line rectifiers for transistorized AC motor
- Field supply for DC motors

Module Type

	Туре		V _{RRM}	V _{RSM}
MDC100G-12	MDA100G-12	MDK100G-12	1200V	1300V
MDC100G-16	MDA100G-16	MDK100G-16	1600V	1700V
MDC100G-18	MDA100G-18	MDK100G-18	1800V	1900V
MDC100G-20	MDA100G-20	MDK100G-20	2000V	2100V

Maximum Ratings

Symbol	Item	Conditions	Values	Unit
IFAV	Average Forward Current	180° Conduction Sin Half Wave, T _c = 105°C	100	A
I _{FRMS}	RMS Forward Current		155	А
I _{FSM}	Surge Forward Current	$T_j = 25^{\circ}C, t = 50Hz(10ms), V_R = 0V$	2500	А
l²t	Circuit Fusing Consideration	t = 10ms T _j =25°C	31250	A ² s
VISO	Isolation Breakdown Voltage	AC 50Hz/60Hz; R.M.S; 1min	3000	V
Tj	Operating Junction Temperature		-40 to +150	°C
T _{stg}	Storage Temperature		-40 to +125	°C
Mt	Mounting Torquo	To Terminals(M5)	3±15%	N·m
Ms	Mounting Torque	To Heatsink(M6)	5±15%	
Weight	Module (Approximately)		105	g

Thermal Characteristics

Symbol	Item	Conditions	Values	Unit
R _{th(j-c)}	Thermal Impedance, Max	Junction to Case(Per Diode)	0.30	°C/W
R _{th(c-s)}	Thermal Impedance, Max	Case to Heat Sink	0.10	°C/W

Electrical Characteristics

Symbol	tem Conditions		Values			Unit
Symbol	Item	Conditions	Min.	Тур.	Max.	Unit
V _{FM}	Forward Voltage Drop, Max	T _j = 25°C I _F = 300A	_	—	1.50	V
I _{RRM} Repetitive Peak Reverse Current, Max	$T_j = 25^{\circ}C$ $V_R = V_{RRM}$		—	0.1	mA	
	$T_i = 150^{\circ}C V_R = V_{RRM}$		—	10		
V _{T0}	Threshold Voltage, for power loss calculation only	T _j = 125°C	0.80		V	
r _T	Slope Resistance, for power loss calculation only	T _j = 125°C	2.2		mΩ	



Circuit		
MDC		
MDA		
MDK	10	



MDC100G

Performance Curves

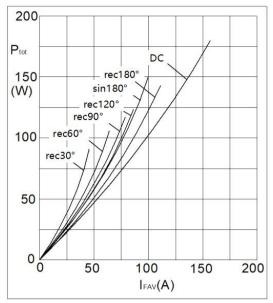


Fig1. Power Dissipation

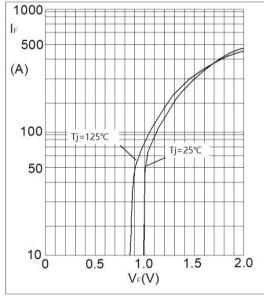


Fig3. Forward Characteristics

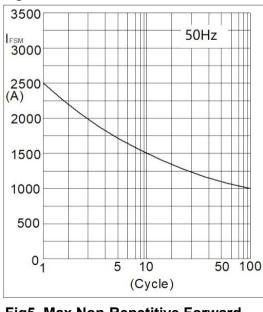


Fig5. Max Non-Repetitive Forward Surge Current

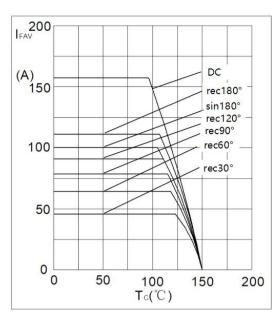


Fig2. Forward Current Derating Curve

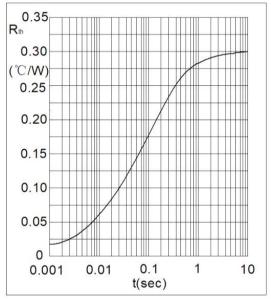
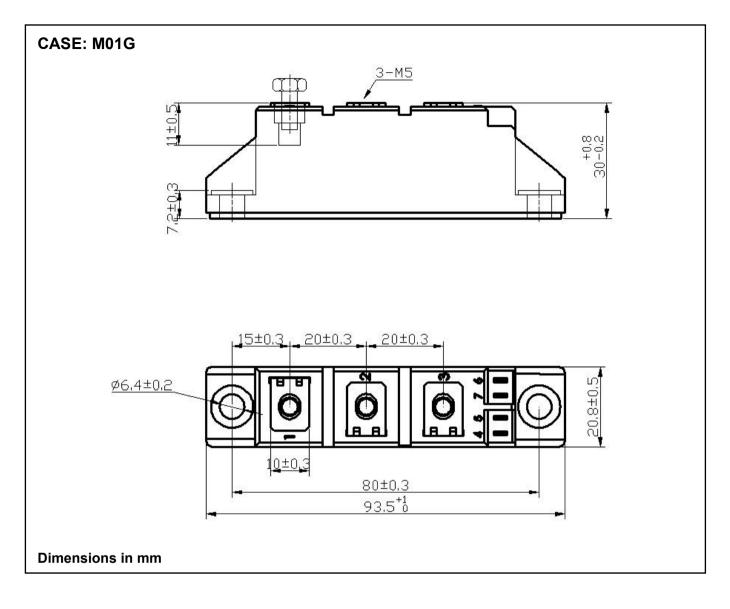


Fig4. Transient Thermal impedance



Package Outline Information





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